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PATENT APPLICATION

Eduard Sackinger

CASE 8

Serial No. 09/498559



Group Art Unit 2816

Filed February 4, 2000

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Technology Center 2100

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Enclosed is an amendment in the above-identified application.

Please charge the amount of \$0 to Lucent Technologies Deposit Account No. 12-2325 to cover the fee as calculated below. Duplicate copies of this letter are enclosed.

CLAIMS AS AMENDED						
	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Rate	Additional Fee
Total Claims for Fee Purposes	19	minus	19	0	x \$18 =	\$0
Independent Claims	3	minus	3	0	x \$84=	\$0
Multiple Dependent Claims first presented with this amendment, if applicable					+ \$280	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:						\$0

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Deposit Account No. 12-2325 as required to correct the error.

Respectfully,

Eugene J. Rosenthal, Attorney
Reg. No. 36658
732-949-1857.

Date: Feb. 14, 2002

Docket Administrator (Room 3J-219)
Lucent Technologies Inc.
101 Crawfords Corner Road
Holmdel, NJ 07733-3030

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Serial No. 09/498559

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Patent Application

Inventor(s): Eduard Sackinger
Case: 8
Serial No.: 09/498,559
Filed: February 4, 2000
Examiner: D. Le
Title: Active Inductor

Group Art Unit: 2816

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TC 2800 MAIL ROOM

Smith

THE COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D.C. 20231

SIR:

AMENDMENT

In response to the Office Action dated September 19, 2001, please amend the above-identified application as follows:

IN THE CLAIMS

Unchanged claim 1

1. An active inductor for use on an integrated circuit having a power supply voltage supplied from a first power supply terminal, comprising:
 - 3 an metal oxide semiconductor (MOS) transistor having a gate terminal, a drain terminal, and a source terminal, said drain terminal being coupled to said power supply voltage and said source terminal being one of the terminals of said active inductor; and
 - 6 a resistor having a first terminal coupled to said gate terminal and a second terminal coupled to a voltage that is derived from said power supply voltage and has a larger absolute value than said power supply voltage supplied by said first power supply terminal and the same sign as said power supply voltage.

Unchanged claim 2

1. The invention as defined in claim 1 wherein the other one of the terminals of said active inductor is said first power supply terminal.